

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Nandakumar et al. Examiner: Edgardo Ortiz
Serial No.: 09/876,292 Art Unit: 2815
Filed: 06/07/2001 Docket: TI-31089
For: **ADDITIONAL N-TYPE LDD/POCKET IMPLANT FOR IMPROVING
SHORT-CHANNEL NMOS ESD ROBUSTNESS**

March 18, 2003

Commissioner for Patents
and Trademarks
Washington, D.C. 20231

FAX RECEIVED

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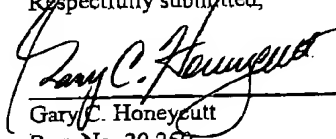
Sir:

TECHNOLOGY CENTER 2800

SUBSTITUTE DRAWINGS

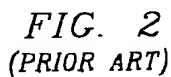
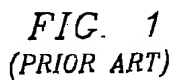
Responsive to a telephone call from Examiner Ortiz, please substitute the enclosed formal drawings.

Respectfully submitted,



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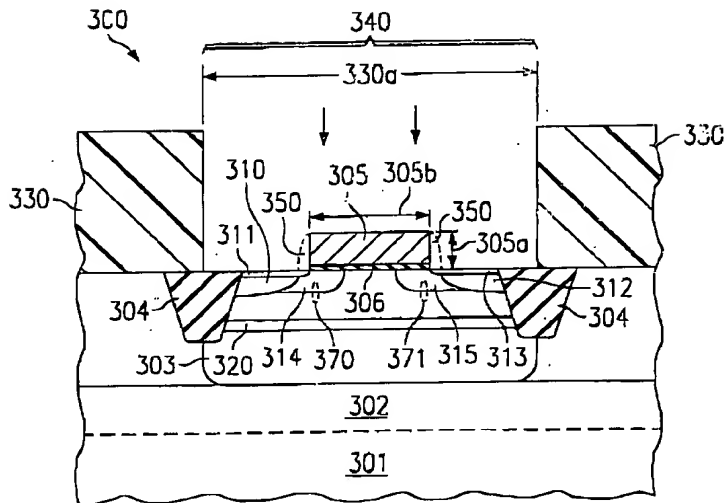


FIG. 3

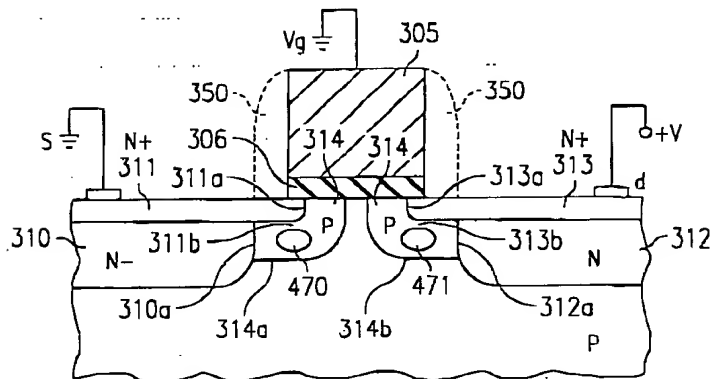


FIG. 4

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FIG. 5

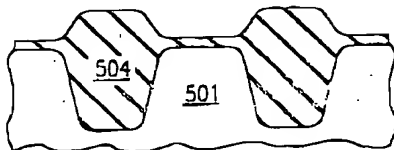


FIG. 6

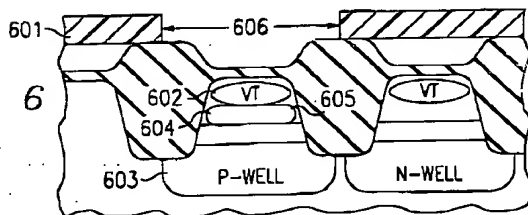


FIG. 7

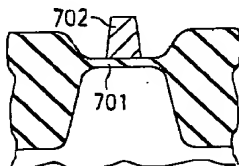


FIG. 8

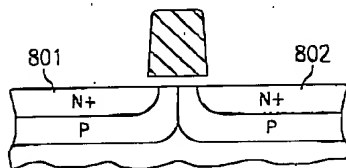


FIG. 9

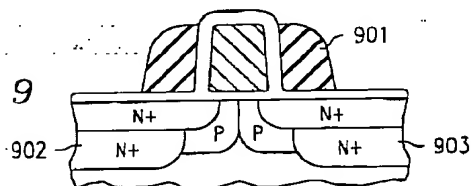


FIG. 10

